



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER, FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov

## \*BIBDATASHEET\*

CONFIRMATION NO. 3893

Bib Data Sheet

SERIAL NUMBER 10/019,407	FILING DATE 03/25/2002  RULE	CLASS 438	GROUP ART UNIT 2813	ATTORNEY DOCKET NO. 520.41003 X00
-----------------------------	---------------------------------------	--------------	------------------------	-----------------------------------------

## APPLICANTS

Yoshinobu Kimura, Tokyo, JAPAN;

 Takahiro Kamo, Mobara, JAPAN;  
 Yoshiyuki Kaneko, Hachioji, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP00/04141 06/23/2000

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 11/181559 06/28/1999

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 16	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 9
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i> INITIALS			
Verified and Acknowledged				

## ADDRESS

 020457  
 ANTONELLI, TERRY, STOUT & KRAUS, LLP  
 1300 NORTH SEVENTEENTH STREET  
 SUITE 1800  
 ARLINGTON, VA  
 22209-9889

## TITLE

Polysilicon semiconductor thin film substrate, method for producing the same, semiconductor device, and electronic device

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input checked="" type="checkbox"/> All Fees
RECEIVED 1524	No. _____ for following:	<input checked="" type="checkbox"/> 1.16 Fees ( Filing )
		<input checked="" type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
		<input checked="" type="checkbox"/> 1.18 Fees ( Issue )